Docket No.: P2002,0542

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : CHARLES SEELEY ET AL.

Filed : CONCURRENTLY HEREWITH

Title : ERROR REPORTING NETWORK IN MULTIPROCESSOR
COMPUTER

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications
are submitted herewith:

PCT WO 02/31656 A2 (Kopetz et al.), dated April 18, 2002;

Patent Abstracts of Japan 54 071 954 (Takashi), dated June 8, 1979;

Author not listed: "Mechanism for Capturing Failure Data with a Replaceable
Machine Element", IBM Technical Disclosure Bulletin, IBM Corp., Vol. 37, No. 01,
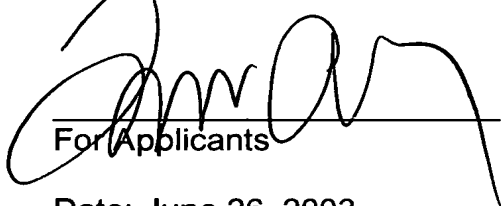January 1994, pp. 163-164;

Kieckhafer, R. et al.: "The MAFT Architecture for Distributed Fault Tolerance",
I.E.E.E. Transactions on Computers, IEEE, Vol. 37, No. 4, April 1988, pp. 398-405;

Sachs, M. W.: "Automatic Transfer of Sense Data to Channel", IBM Technical
Disclosure Bulletin, IBM Corp., Vol. 25, No. 3B, August 1982, pp. 1738-1739;

Johnson, D. B.: "Error Reporting in the Intel iAPX 432", IEEE Comp. Soc. Press,
1984, pp. 24-28.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

For Applicants

LAURENCE A. GREENBERG
REG. NO. 29,308

Date: June 26, 2003

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

/kf

FORM PTO-1449 (SUBSTITUTE)	Attorney Docket No.: P2002,0542
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Appl. No.: Applicant: CHARLES SEELEY ET AL. Filing Date: June 26, 2003 Group Art Unit:

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
	J	02/31656 A2	4/18/02	WIPO				
	K	54071954	6/8/79	Japan				
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

		Author not listed: "Mechanism for Capturing Failure Data with a Replaceable Machine Element", IBM Technical Disclosure Bulletin, IBM Corp., Vol. 37, No. 01, January 1994, pp. 163-164
		Kieckhafer, R. et al.: "The MAFT Architecture for Distributed Fault Tolerance", I.E.E.E. Transactions on Computers, IEEE, Vol. 37, No. 4, April 1988, pp. 398-405

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: P2002,0542 Appl. No.: Applicant: CHARLES SEELEY ET AL. Filing Date: June 26, 2003 Group Art Unit:

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
	J							
	K							
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

		Sachs, M. W.: "Automatic Transfer of Sense Data to Channel", IBM Technical Disclosure Bulletin, IBM Corp., Vol. 25, No. 3B, August 1982, pp. 1738-1739
		Johnson, D. B.: "Error Reporting in the Intel iAPX 432", IEEE Comp. Soc. Press, 1984, pp. 24-28

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.